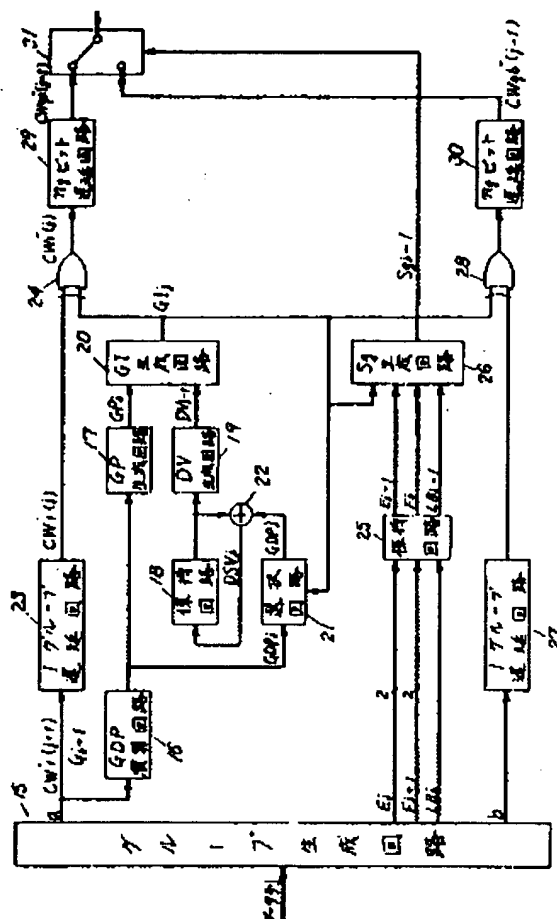




Help

☐ Include in patent order**MicroPatent® Worldwide PatSearch:** Record 1 of 1

BEST AVAILABLE COPY

Family Lookup

JP62188073

CODE CONVERSION DEVICE

MATSUSHITA ELECTRIC IND CO LTD

Inventor(s): ;IKETANI AKIRA

Application No. 60296778 , Filed 19851227 , Published 19870817

Abstract:

PURPOSE: To realize a DC free RLL code adapted for recording with a density higher than ever in a real circuit scale, by dividing and generating a code word in a DC free code, and setting the DC free code as the DC free RLL code in which the number of continuous bits of same binary values is limited not more than (k) and not less than (d).

CONSTITUTION: A data word string is processed at a group generation circuit 15, and the first code work $CW_{1(j+1)}$ in a group G_{j+1} is outputted, and also, the forefront bit of a code work $CW_{1(j)}$ appears from a delay circuit 23, and at a gate 24,

the forefront bit of a $CW_{1(j)}$ appears corresponding to the output GI_j of a circuit 20, and the forefront bit of a $CW_{ga(j-1)}$ appears from a delay circuit 29. A code word $CW_{gb'(j-1)}$ in which the above value is equal to the values of parameters F and E in blocks L and R, and the binary value consisting of the block R is different, can be obtained through the output (b) of the circuit 15, and circuits 27 and 20. A switch 31 selects and outputs code words $CW_{ga''(j-1)}$ and $CW_{gb''(j-1)}$ through the above value and a control signal S_{gi-1} from a circuit 26, and the DC free code in which the number of continuous bits is limited from the (d) up to the (k), can be obtained.

COPYRIGHT: (C)1987,JPO&Japio

Int'l Class: G11B02014 H03M00714

MicroPatent Reference Number: 000157569

COPYRIGHT: (C) JPO



PatentWeb
Home



Edit
Search



Return to
Patent List



Help

For further information, please contact:
[Technical Support](#) | [Billing](#) | [Sales](#) | [General Information](#)